Docket No.: 1514.1028

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Ki Yong LEE

Serial No. 10/677,278 Group Art Unit: 2891

Confirmation No. 8756

Filed: October 3, 2003 Examiner: Wilson, Christian D.

For: METHOD FOR FABRICATING THIN FILM TRANSISTOR USING DUAL OR MULTIPLE GATES

AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed May 23, 2005, and having a period for response set to expire on August 23, 2005.

Reconsideration of the claims is respectfully requested. The following remarks are respectfully submitted.